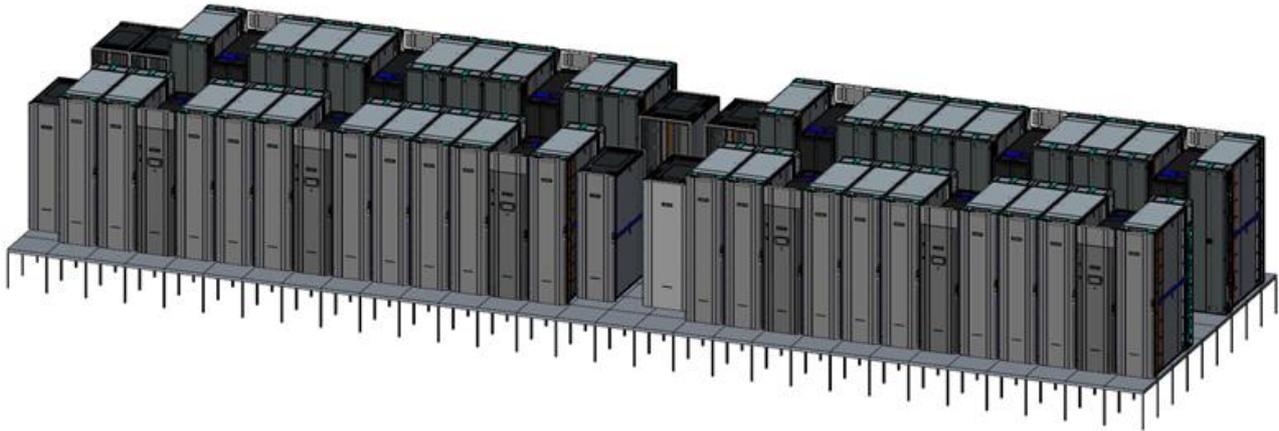


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HPE and Sandia to build world's largest Arm supercomputer

18 June 2018 By [Sebastian Moss](#)



145,000-core Astra will be used to model nuclear decay

Hewlett Packard Enterprise will collaborate with Sandia National Laboratories and the US Department of Energy to build the world's largest Arm-based supercomputer.

Based on the HPE Apollo 70 system, 'Astra' will feature 2,592 dual-processor servers with 145,000 Cavium ThunderX2 cores and deliver 2.322 theoretical peak petaflops of performance. The nodes will consume 1.2MW of power, cooled by HPE's MCS-300 Cooling Units.

Astra will be installed at Sandia, in an expanded part of the building that originally housed the Red Storm supercomputer, an innovative system that we profiled [earlier this year](#) ([URL=http://www.datacenterdynamics.com/content-tracks/servers-storage/after-the-storm-the-supercomputer-that-saved-cray/99986.fullarticle](http://www.datacenterdynamics.com/content-tracks/servers-storage/after-the-storm-the-supercomputer-that-saved-cray/99986.fullarticle)). The Arm-based supercomputer will be used by the National Nuclear Security Administration (NNSA) to run advanced modeling and simulation workloads.

Computer Armaments

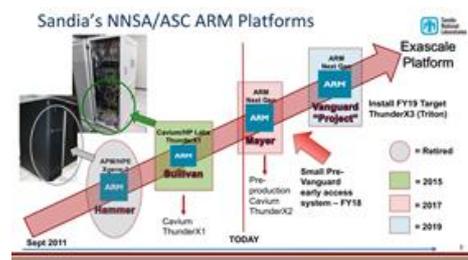
"One of the important questions Astra will help us answer is how well does the peak performance of this architecture translate into real performance for our mission applications," Mark Anderson, program director for NNSA's Advanced Simulation and Computing program, said.

Scott Collis, director of Sandia's Center for Computing Research, added: "Emerging architectures come with many challenges.

"Since the NNSA has not previously deployed high-performance computing platforms based on Arm processors, there are gaps in the software that must be addressed before considering this technology for future platforms much larger in scale than Astra."

Researchers anticipate that they will continue to improve and upgrade the hardware. The supercomputer is part of the NNSA's Vanguard project, a multi-system initiative to build various Arm computers, with the aim of creating and funding the necessary tools and technologies "to enable a credible Arm-based Advanced Technology System offering for ASC post 2022," a [2017 presentation noted](#) ([URL=http://www.goingarm.com/slides/2017/ArmRSHSummit2017/GoingArm_RSHSummit2017_Sandia.pdf](http://www.goingarm.com/slides/2017/ArmRSHSummit2017/GoingArm_RSHSummit2017_Sandia.pdf))

Ken Alvin, senior manager of Sandia's extreme scale computing group, said: "The Vanguard program is designed to allow the NNSA to take prudent risks in exploring emerging technologies and broadening our future computing options."



Sandia's NNSA/ASC Arm rollout plan (2017)

Source: [Sandia National Laboratory](#).

Arm has struggled to make headway in the data center, with Qualcomm, one of the companies developing the architecture, recently announcing lay-offs [in its server division](http://www.datacenterdynamics.com/content-tracks/servers-storage/qualcomm-says-it-loves-data-centers-plans-to-cut-half-of-data-center-division/100269.fullarticle) (URL=<http://www.datacenterdynamics.com/content-tracks/servers-storage/qualcomm-says-it-loves-data-centers-plans-to-cut-half-of-data-center-division/100269.fullarticle>). But the low-power properties of Arm chips [have found some success](http://www.datacenterdynamics.com/content-tracks/open-data-center/arm-reveals-its-data-center-super-powers/96851.fullarticle) (URL=<http://www.datacenterdynamics.com/content-tracks/open-data-center/arm-reveals-its-data-center-super-powers/96851.fullarticle>) in the the high performance computing space.

Cavium processors are used in prototypes for the [Mont-Blanc project](http://www.datacenterdynamics.com/content-tracks/servers-storage/mont-blanc-eu-supercomputer-picks-cavium-thunderx2-cpus/97611.fullarticle) (URL=<http://www.datacenterdynamics.com/content-tracks/servers-storage/mont-blanc-eu-supercomputer-picks-cavium-thunderx2-cpus/97611.fullarticle>), an initiative supported by the European Union that aims to create compute nodes for an Exascale-class supercomputer.

Arm is also being used [in Catalyst UK](http://www.datacenterdynamics.com/content-tracks/servers-storage/catalyst-uk-to-build-12000-core-arm-based-supercomputer/100027.fullarticle) (URL=<http://www.datacenterdynamics.com/content-tracks/servers-storage/catalyst-uk-to-build-12000-core-arm-based-supercomputer/100027.fullarticle>), a 12,000-core machine that will be distributed across clusters located at the University of Edinburgh, the University of Bristol, and the University of Leicester.

But the biggest deployment will arrive as part of Japan's Post-K supercomputer, a 1,000-petaflop next generation system engineered by Fujitsu. It is currently set to launch in 2021-2022.